Fluorinated thin gate oxides prepared by room temperature deposition followed by furnace oxidation

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Received 1 December 1997; received in revised form 1 July 1998

Abstract

Room temperature deposition in H\textsubscript{2}SiF\textsubscript{6} solution, i.e., liquid phase deposition (LPD), followed by furnace oxidation (FO) is first used to prepare fluorinated thin oxides (LPD/FO). The amount of fluorine existing in the gate oxide is controlled by varying the LPD time in this work. A turnaround breakdown behavior is observed for LPD/FO oxides with various LPD conditions. The oxide with an optimized fluorine concentration shows a significant improvement in oxide breakdown characteristics.

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1. Introduction

A key issue of ultra-large-scale integration technology (ULSI) is the quality of thin silicon dioxide layer used in device. Today, oxide thickness less than 80 Å and oxidation temperatures at about 800–900°C are required as the semiconductor industry pushes toward 1 Gbit memory chips and beyond. However, good electric characteristic generally needs high temperature treatment either in oxidation or in postoxidation annealing (POA). At low temperatures, ultrathin SiO\textsubscript{2} films prepared by the conventional furnace oxidation method have some problems, such as high-density dangling bonds at the interfaces, large fixed charge density and more pinholes in the bulk. To solve these problems, the incorporation of small amounts of F into SiO\textsubscript{2} was demonstrated to be efficient to improve the SiO\textsubscript{2}/Si interface hardness against hot-electron or radiation damage [1–5]. There are a number of ways to introduce fluorine into the gate oxide, including in-situ NF\textsubscript{3} oxidation [3], diffusion with a fluorine-rich LPCVD tungsten silicide film [6], and ion implantation [4]. Recently, a room-temperature liquid phase deposition (LPD) method is proposed to prepare fluorine-enriched oxides [7]. In this work, high-temperature furnace oxidation (FO) treatment to LPD fluorinated oxides is studied.

It is known that liquid phase deposition (LPD) is a room-temperature method for SiO\textsubscript{2} formation. To date, LPD-oxide films can be deposited by adding either H\textsubscript{3}BO\textsubscript{3} [8] or Al [7] to supersaturated hydrofluorosilicic acid (H\textsubscript{2}SiF\textsubscript{6}). However, these methods suffer from the chemical contamination and make the deposition reaction in LPD complicated. In addition, the deposition rate is also too fast to grow high-density thin oxides by these ways. For this reason, the LPD method with H\textsubscript{2}O addition only is used in this work. It is known that the amount of F incorporated in gate oxide can be controlled by the initial LPD growth time. The following FO treatment time and temperature will also affect the F concentration in fluorinated oxides, since the F atoms introduced by LPD process can escape from fluorinated gate oxides in the form of SiOF\textsuperscript{+} at a threshold temperature of 550°C or in the form of SiF\textsubscript{3}\textsuperscript{+} at a threshold temperature of 600°C [9]. To study the effect of fluorine on the electrical properties of thin fluorinated oxides, the LPD/FO oxides with different LPD time followed by furnace oxidation (900°C) treatment are examined. It is found that the flat-band voltage $V_{fb}$ and midgap interface trap density

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$D_{\text{min}}$ for LPD/FO oxides are strongly dependent on LPD conditions. Also, it is observed that both the charge-to-breakdown, $Q_{\text{BD}}$, and oxide breakdown field, $E_{\text{min}}$, characteristics of LPD/FO samples exhibit turnaround dependency on LPD growth time. The oxide quality is controllable by adjusting the LPD time in LPD/FO oxides.

2. Experimental

The experimental flowchart for preparing the immersion solution and the LPD samples is similar to the previous work [8]. Briefly, the preparation of the LPD sample needs four steps as follows. To start with, the industrial grade hydrofluorosilicic acid (H$_2$SiF$_6$) with a concentration of 3.09 mol l$^{-1}$ is used. Then, the silica-water solution was added to consume the residual hydrofluoric acid (HF). After this, the mixed solution was filtered by a 1 μm filter paper to remove the undissolved gel-formed silica powder after stirring the solution by magnetic stirrer for more than 3 h at 25°C. Now, the H$_2$SiF$_6$ solution is saturated with silica and ready for oxide deposition. Before starting to deposit the LPD oxide, a certain quantity of deionized (DI) water was added to the saturated solution. The adding DI water to the saturated solution is necessary to initiate the LPD oxide deposition. It is noted that the quantity of DI water added will make influence on the deposition rate, growth delay time and electrical properties [8]. The large amount of DI water added to saturated solution will result in high deposition rate and poor oxide quality [10]. To obtain good oxide quality, a relatively low deposition rate is suggested. The volume ratio of saturated H$_2$SiF$_6$ solution to DI water is 100 ml to 25 ml in this work.

The MOS capacitors tested in this work are fabricated on 4 in. (100) P-type silicon substrates with a resistivity of 7–10 Ω cm. After standard RCA clean (NH$_4$OH:H$_2$O$_2$:H$_2$O = 0:1:1:5) followed by 50:1 H$_2$O:HF dip and DI water rinse, wafers were put in the above-mentioned H$_2$SiF$_6$ solution for 10, 20, 40, and 60 min to deposit various LPD oxides at 25°C with thickness ranging from 5 to 20 Å. Then, the above wafers in accompany with one control sample, i.e., bare Si wafer, were furnace oxidized in pure dry O$_2$ gas at 900°C followed by an in situ POA anneal in N$_2$ for 10 min. These LPD/FO samples are labeled as L10/FO, L20/FO, L40/FO, L60/FO and control.

Aluminum film is evaporated onto all samples to serve as metal gate electrodes in this work. After photolithography, back side oxides are stripped and followed by metallization. The samples employed for measurement of C–F characteristics are annealed by postmetallization (PMA) in pure N$_2$ ambient at 400°C for 10 min, and those used for oxide reliability tests don’t receive such PMA treatment to avoid Al penetration. The area of the MOS capacitors used for reliability characterization is 1 × 10$^{-2}$ cm$^2$. The oxide film thickness is measured by ellipsometry with a fixed refractive index of 1.46. In time zero dielectric breakdown (TZDB) tests, a 0.5 V s$^{-1}$ staircase ramp voltage is stressed and the oxide breakdown field is defined as the electric field at which the current density across the oxide exceeds 0.5 A cm$^{-2}$. For time dependent dielectric breakdown (TDDB) tests, Fowler–Nordheim (F–N) injection with a constant current density of 10 mA cm$^{-2}$ is performed on the MOS capacitors. It is noted that all reliability tests in this work are in accumulation electron injection mode, i.e., a negative gate bias for all MOS capacitors.

3. Electrical characteristics of the LPD/FO oxides

The initial LPD oxide properties are first examined. From SIMS profiles (not shown in this work), it is observed that longer LPD growth time introduces a larger amount of F incorporated in the gate oxide. It is believed that different F densities and distributions will result in different electrical properties. Fig. 1 shows the flat-band voltage, $V_{fb}$, and the midgap interface trap density, $D_{\text{min}}$, for the control, L10/FO, L20/FO, and L60/FO samples. It is observed that all the $V_{fb}$ and $D_{\text{min}}$ increase with LPD growth time, i.e., the number of F atoms incorporated in oxide changes with LPD time. It is supposed that two-step processes occur in the LPD/FO oxides during FO treatment. In the first step, the F atoms will escape from the initial LPD oxides by diffusing into and out of the gate oxide in the form of SiOF$^+$ or SiF$_x^+$, which will produce a number of defects and dangling bonds in the bulk oxide [9]. Some of the F atoms will replace Si–OH or Si–H bonds by much stronger Si–F bonds. On the
other hand, the decomposition of Si–OH bonds existing in LPD oxides will lead to the existence of H\(^+\) or oxygen vacancies O\(_{3/2}\) Si\(_{3/2}\) in the oxide film at the same time [11]. It results in the increase of \(D_{\text{itm}}\) and positive charges at Si/SiO\(_2\) interface, which makes the left shift in the \(C-V\) curves for all LPD/FO samples. Consequently, in this step, it possibly induces higher interface trap density from oxygen vacancies and more disordered fixed charge \(Q_f\) distribution at the interface. In the second step, these produced defects and oxygen vacancies will be passivated by oxygen atoms to form Si–O bonds. This effect will result in the reduction of the defects and positive charges. However, not all of the defects and oxygen vacancies can be passivated in this step. It depends on the initial LPD growth time and the following treatment time and temperature. By this two step mechanism, the trends in \(V_{\text{fb}}\) and \(D_{\text{itm}}\) can be explained. When the LPD oxide fraction is small, i.e., the LPD oxide growth time shorter than 20 min, the defects are not so much that the oxygen can restructure these dangling bonds. Hence, the variation of \(V_{\text{fb}}\) and \(D_{\text{itm}}\) are similar to the control oxide for LPD/FO oxides with small LPD fractions. As the fraction of LPD oxide increases, the dangling bonds will become too many to be passivated by oxygen and the shifts in \(V_{\text{fb}}\) and \(D_{\text{itm}}\) become large.

However, it has been reported that incorporating F into oxides with implantation [12, 4] or in-situ oxidation [13] method can reduce \(D_{\text{itm}}\). This is contrary to the observation of this work. The possible reason is that the LPD oxides may decompose into Si–OH. This decomposition introduces the oxygen vacancies which are the precursors of interface states [14]. The H\(^+\) release will induce some positive charges in oxides. Thus, it will result in large \(D_{\text{itm}}\) and \(V_{\text{fb}}\) in LPD/FO oxides. In addition, it is supposed that the high-temperature process (~900°C) may be another reason for the anomalous effect observed in this work since it makes more F evaporate and leaves less Si–F bonds in the interface.

The effect of F fraction on the resistance to hot-electron damage is examined by a constant Fowler–Nordheim current stress with a current density of 10 \(\mu\)A cm\(^{-2}\) for 200 s, i.e., a total charge of 0.2 mC cm\(^{-2}\), under gate injection (~\(V_g\)). In Fig. 2, the changes of flat-band voltage \(\Delta V_{\text{fb}}\) and midgap interface trap den-

![Fig. 2. The flat-band voltage shift \(\Delta V_{\text{fb}}\) and the midgap interface trap density shift \(\Delta D_{\text{itm}}\) after constant current stress for various LPD/FO oxides.](image)

![Fig. 3. Weibull plots of charge-to-breakdown \(Q_{\text{BD}}\) for various LPD/FO oxides. The inset is gate voltage versus stress time for various LPD/FO oxides during constant current stress.](image)
sity $\Delta D_{\text{lin}}$ after stressing are plotted for various LPD/FO oxides. It is found that the $\Delta V_{\text{fb}}$ and $\Delta D_{\text{lin}}$ for the LPD/FO oxides with high fluorine content are seriously degraded. However, it is worth noticing that for the sample with a suitable LPD condition, i.e., the L20/FO sample, there exists a higher resistance to hot-electron injection than control one. It is known that the amount of F incorporated into SiO$_2$ should play an important role in determining the resistance of the SiO$_2$/Si interface against hot-carrier damage. As F exists in the oxide, it will improve the interface because of strain relaxation and replacing Si–H bonds by the much stronger Si–F bonds. However, if the interface regions have been saturated with fluorine, the additional incorporation of F will be harmful to oxides. The extra F atoms in the bulk may cleave the Si–O bonds and displace oxygen at these sites [12, 13]. It is speculated that these interfacial nonbridging oxygen centers, after giving up an electron to Si by the displacement of F, may be a candidate for positive fixed charge. This step increases and redistributes the bulk fixed charges for all LPD/FO oxides more or less. The negative flat-band shift for all LPD/FO oxides is also explainable by this way.

Fig. 3 show the Weibull plots of the $Q_{\text{BD}}$ for various LPD/FO oxides with a similar final oxide thickness of 80 Å. It is observed that the $Q_{\text{BD}}$’s exhibit turnaround dependency on LPD growth time. The L20/FO sample exhibits a higher $Q_{\text{BD}}$ than the control oxide. It has been reported that charge trapping and trap generation are the key processes in dielectric breakdown [15–19]. Because of the large stress at the Si/SiO$_2$ interface, the strained interface is inherently weaker than the bulk oxide and the trapping is believed to be more severe at the interface [16, 17, 20–22]. In order to examine the generation of new oxide traps under high-field hot-electron injection, the gate voltage versus stress time during a constant Fowler–Nordheim current stressing is also shown in the inset of Fig. 3 for comparison. The electron trapping behavior is observed due to negative electric field shift. A non-linear electric field increases due to the charging of initial traps [23]. From the inset, it indicates that the voltage shift of the low fraction LPD/FO oxide is similar to that of the high fraction one but only a little larger than the control oxide. It is known that a non-saturating quasi-linear electric field increases corresponding to the generation of new electron traps and their partial occupation [17]. Although the LPD/FO oxides exhibit more electron traps, their inclinations to the generation of new oxide traps are nearly identical to the control oxide except for the highest fraction LPD/FO one. Furthermore, it is worth noticing that the LPD/FO oxides sustain lower electric field for constant current stress than control oxide does, which implies that the LPD/FO oxide exists larger leakage current than control oxide. The Weibull plots of the $E_{\text{BD}}$ for various LPD/FO oxides are shown in Fig. 4. It is noted that the $E_{\text{BD}}$ of control oxide is larger than all LPD/FO oxides. The typical $J$–$V$ characteristics of the LPD/FO and control oxides are also shown in the inset of Fig. 4 for comparison. It is observed that the leakage currents of LPD/FO ox-
ides are larger than that of control one. It is believed that they are strongly related to the fraction of F.

4. Discussion

There are numerous models regarding the breakdown mechanism, including the so-called hole-induced breakdown model [24–27], the electron-trapping breakdown model [17], the interface-trap generation and resonant-tunneling-induced breakdown models [28] as well as the anode hole injection breakdown model [29–35]. Although there is no strong consensus concerning the breakdown mechanism, it is generally accepted that the breakdown is a two step process, i.e., wearout followed by breakdown. Wearout is caused by trap generation inside the oxide. Breakdown is induced by local high current regions, which are triggered by a local increase in the current density. In other words, the occurrence of TIDDB mainly relies on the distribution and generation of oxide trap number density $N_{ot}$ but not on the initial effective oxide charges.

For constant current TIDDB test of LPD/FO oxides, two major factors compete with each other as the LPD oxide fraction increases. One is the addition of induced electron traps and defects, and the other is the enhancement of the existence of Si–F bonds. The former causes larger a electric field in the interface and the latter intensifies the endurance to the impact ionization. As a result, the turnaround behavior of $Q_{BD}$ is observed. It means that the reliability of the LPD/FO oxides is strongly dependent on the fraction of LPD oxides. Since the F can replace Si–H or Si–OH and compensate dangling bonds in the form of stronger Si–F bonds, some LPD/FO oxides have higher reliability than control ones. For the TIDDB test of LPD/FO oxides, there are also two major factors competing with each other as the LPD oxide fraction increases. One is the addition of induced electron traps and the other is the increase of the disorder of the fixed charge distribution. Between these two factors, the former leads to larger $E_{BD}$ and the latter causes the localized higher field to lower the $E_{BD}$. For this reason, the turnaround behavior will occur. It emphasizes again that there is a strong relation between the reliability and the fraction of initial LPD oxides for LPD/FO samples.

5. Conclusion

In this work, the fraction effect of F atoms in LPD/FO oxides by varying the initial LPD time has been studied. The most suitable F fraction for preparing 80 Å oxides at 900°C in this work is L20/FO, which gets a larger $Q_{BD}$ and a higher resistance to hot-electron than control one. However, a turnaround breakdown behavior exists in LPD/FO oxides. Too long LPD growth time will degrade the quality of the oxides in all aspects, possibly due to the too much left oxygen vacancies from the evaporation of F and decomposition of Si–OH bonds. Nevertheless, the room temperature LPD followed by FO provides a new method to prepare thin fluorinated gate oxides. The controllability of F content in LPD/FO oxides by adjusting LPD time is attractive and is worthy of studying in the near future.

Acknowledgements

The authors want to thank the National Science Council of the Republic of China for supporting this work under Contract No. NSC86-2215-E-002-020.

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