Noise Reduction Using Compensation Capacitance for Bend Discontinuities of Differential Transmission Lines

Guang-Hwa Shue, Wei-Da Guo, Chien-Min Lin, Member, IEEE, and Ruey-Beei Wu, Senior Member, IEEE

Abstract—Differential signaling has become a popular choice for multigigabit digital applications in favor of its low-noise generation and high common-mode noise immunity. Recalling from the full-wave solution of S-parameters, this paper presented a design methodology of analysis scheme to extract the equivalent circuits of discontinuities observed on the strongly coupled differential lines. Signal integrity effects of the bent differential transmission lines in a high-speed digital circuit were then simulated in the time domain. A dual back-to-back routing topology of bent differential lines to reduce the common-mode noise was further investigated. To alleviate the common-mode noise at the receiver, a novel compensation scheme in use of the shunt capacitance was also proposed. Furthermore, the comparison between the simulation and measurement results validated the equivalent circuit model, coupled bends with compensation capacitance patch, and analysis approach.

Index Terms—Common-mode noise, compensation capacitance, differential signaling, discontinuity, dual back-to-back coupled bends, signal integrity, strongly coupled differential line, mixed-mode S-parameters.

I. INTRODUCTION

WITH the increasing density of digital circuit layouts and faster pulse edges, the bend discontinuities of microstrip and stripline structures have now been shown to introduce noticeable signal degradations for short rise-time pulses. In the past, various single-line bends have been investigated on the S-parameters [1], [2], lumped element equivalent circuit model extraction [3], [4], and radiation effect [5], [6]. Considering the serial data links for high-speed multigigabit per second digital circuitry (such as IEEE 1394, USB 2.0, Serial ATA, and PCI-Express), differential signaling is usually used to improve the signal integrity and reduce the electromagnetic interference (EMI). The routing scheme is in favor of its low-noise generation and high common-mode noise immunity.

For an ideal serial data link, the differential interconnect maintains the differential symmetry and, therefore, the differential to common-mode conversion is not an issue. In a practical layout scenario, the interconnect topology is nonetheless asymmetrical due to the bends, presence of adjacent traces, and physical constraints in routing near the package and connectors. The impact of signal integrity and EMI [7] on the amount of common-mode noise generated by the phase skew of different routing lengths should be considered as well.

In recent years, various methods have been adopted to model a multiconductor microstrip using the T or π model, such as the method of moments [8], method of lines [9], analysis with the efficient excess-charge and excess-current techniques [10], model extraction from the time-domain reflection/transmission (TDR/T) measurement method [11], and three-dimensional (3-D) transmission-line matrix method [12]. However, these papers did not investigate the common-mode noise induced by differential bends and noise reduction scheme. In this paper, the feasible design methodology is hereby proposed to explore the signal integrity analysis for the discontinuity structures according to the growing demands on high-speed interconnects.

Starting from the calculated S-parameters of strongly coupled differential lines by the full-wave solver IE3D,1 the equivalent circuit model of bend discontinuities is extracted as described in Section II. Various bent differential transmission lines in a high-speed digital circuits is outlined in Section III. Their signal integrity effects are then analyzed in the time domain to characterize the parasitic data and common-mode and differential-mode noise profiles. As a noise reduction scheme in Section IV, both the dual coupled bends design and the detour design for reducing the common-mode noise in its microstrip structure are investigated. A novel compensation scheme using the shunt capacitance is proposed to alleviate the common-mode noise at the receiver. In Section V, favorable comparisons between the simulation and measurement results in the time-domain and frequency-domain validate the equivalent circuit model and analysis approach. The overview summary for this paper is in Section VI.

II. THEORY AND CIRCUIT MODELING

A. Model Setup

To investigate the signal integrity issues of bend discontinuities, it is necessary to develop the lumped and frequency-dependent equivalent circuits for these configurations. Considering the bend discontinuity of differential lines for its layout size to be much smaller than the wavelength of operating frequency.

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Fig. 1. Graphical representation of bent coupled transmission lines. (a) Physical layout. (b) Block diagram. (c) Lumped equivalent circuit model.

range as depicted in Fig. 1(a), the block diagram of coupled transmission lines can be modeled as shown in Fig. 1(b), where the bend can be approximated by a simple lumped circuit in reference to Fig. 1(c). This lumped circuit model is an extension of the π model used for a single transmission line. Though this model can include the radiation, conductor and dielectric losses, and frequency-dependent resistance, these losses are small and negligible for practical applications up to gigabit per second rates.

B. De-Embedding Tx Line Effects

Most of the present commercial electromagnetic field solvers assume the isolated output ports in deembedding the transmission line effects. However, when the input and output transmission lines are strongly coupled, a more general approach needs to be developed. For simplicity, a system of $N$ bent coupled transmission lines is considered here. By using the field solvers, its full-wave characteristics can be modeled in terms of a $2N$-port $ABCD$ matrix. Based on the cascade property, the $ABCD$ matrix can be divided into three parts: two sections of coupled transmission lines and a bend structure between.

For the block diagram as depicted in Fig. 1(b), the $ABCD$ matrix of bent differential transmission lines at the output port $D$ in response to the input port $A$ can be formulated as

$$
\begin{bmatrix}
V_D \\
I_D
\end{bmatrix} = \begin{bmatrix}
\tilde{A}_D & -\tilde{B}_D \\
-\tilde{C}_D & \tilde{D}_D
\end{bmatrix}^{-1} \cdot \begin{bmatrix}
\tilde{A}_b & \tilde{B}_b \\
\tilde{C}_b & \tilde{D}_b
\end{bmatrix} \cdot \begin{bmatrix}
\tilde{A}_c & \tilde{B}_c \\
\tilde{C}_c & \tilde{D}_c
\end{bmatrix} \cdot \begin{bmatrix}
V_A \\
I_A
\end{bmatrix}
$$

(1)

where the subscript “$b$” denotes the bend discontinuities, and “$\ell$” denotes a section of the coupled transmission lines with length $\ell$. To deembed the effects of transmission lines, a section of coupled transmission lines of straight length $2\ell$ is considered,
and its ABCD matrix without the bend discontinuities can be formulated by

\[
\begin{bmatrix}
\vec{V}_D \\
\vec{I}_D
\end{bmatrix}_{\text{without bend}} = \begin{bmatrix}
\vec{A}_\ell \\
\vec{C}_\ell
\end{bmatrix}^{-1} \cdot \begin{bmatrix}
\vec{A}_\ell \\
\vec{B}_\ell
\end{bmatrix} \cdot \begin{bmatrix}
\vec{V}_A \\
\vec{I}_A
\end{bmatrix}.
\]

(2)

The ABCD_\ell matrix can be solved from (2) and then substituted into (1) to extract the desired ABCD_b matrix for coupled bend discontinuities.

C. Lumped Circuit Model Extraction

Given the ABCD_b matrix of coupled bend discontinuities, the corresponding S-matrix can be obtained and written as

\[
\begin{bmatrix}
b_1 \\
b_2 \\
b_3 \\
b_4
\end{bmatrix} = \begin{bmatrix}
S_{11} & S_{12} & S_{13} & S_{14} \\
S_{21} & S_{22} & S_{23} & S_{24} \\
S_{31} & S_{32} & S_{33} & S_{34} \\
S_{41} & S_{42} & S_{43} & S_{44}
\end{bmatrix} \begin{bmatrix}
a_1 \\
a_2 \\
a_3 \\
a_4
\end{bmatrix}
\]

\[
= \begin{bmatrix}
\alpha_{11} & \alpha_{12} & 1 + \alpha_{13} & \alpha_{14} \\
\alpha_{21} & \alpha_{22} & 1 + \alpha_{23} & \alpha_{24} \\
1 + \alpha_{31} & \alpha_{32} & \alpha_{33} & \alpha_{34} \\
\alpha_{41} & 1 + \alpha_{42} & \alpha_{43} & \alpha_{44}
\end{bmatrix}
\begin{bmatrix}
a_1 \\
a_2 \\
a_3 \\
a_4
\end{bmatrix}
\]

(3)

where \(\alpha_i\)'s are small quantities if the size of discontinuity is much smaller than one wavelength of the operating frequency range. It is noteworthy that the \(\alpha_i\)'s satisfy \(\alpha_{ij} = \alpha_{ji}\) due to reciprocity, and in addition \(\alpha_{11} = \alpha_{33}, \alpha_{22} = \alpha_{44}, \alpha_{23} = \alpha_{14}\), and \(\alpha_{12} = \alpha_{34}\) due to the structural symmetry. As a result, there are only six independent \(\alpha_i\)'s in (3).

While all the terms corresponding to higher orders of \(\alpha_i\)'s are negligible, the \(\alpha_i\)'s can be related to the lumped circuit elements of Fig. 1(c) in light of Kirchoff’s Current Law by

\[
\alpha_{11} - \alpha_{13} = \frac{(R_{13} + j\omega L_{13,s})}{Z_0}
\]

(4a)

\[
\alpha_{22} - \alpha_{24} = \frac{(R_{24} + j\omega L_{24,s})}{Z_0}
\]

(4b)

\[
\alpha_{12} - \alpha_{14} = \frac{(R_{12} + j\omega L_m)}{Z_0}
\]

(4c)

\[
-\frac{(\alpha_{11} + \alpha_{13} + \alpha_{12} + \alpha_{14})}{2} = (G_{11} + j\omega C_{11,s}) \times Z_0
\]

(4d)

\[
-\frac{(\alpha_{22} + \alpha_{24} + \alpha_{12} + \alpha_{14})}{2} = (G_{22} + j\omega C_{22,s}) \times Z_0
\]

(4e)

\[
-\frac{(\alpha_{12} + \alpha_{14})}{2} = (G_{12} + j\omega C_{12,m}) \times Z_0
\]

(4f)

where the subscript “s” denotes the self terms, and “m” denotes the mutual terms. Note that \(R_{ij}\) and \(G_{ij}\) are not shown in Fig. 1(c). Since they are usually small and can be neglected in the practical frequency range.

III. TRANSIENT ANALYSES

For various bent differential transmission line structures, the transient analyses using the full-wave solver IE3D at the frequency of 2 GHz are presented in this section. These structures include the coupled right-angle bends, right-angle with miter bends, round-corner bends, and 45-degree-angle bends, while the substrate dielectric constant \(\varepsilon_r = 4.3\), height \(h = 1.5\) mm, microstrip width \(w = 1.3\) mm, thickness \(T = 0.125\) mm, and center-to-center separation \(s = 1.8\) mm as depicted in Fig. 2. Following the aforementioned procedures in Section II, the lumped equivalent circuit parameters corresponding to the various bend structures are extracted and summarized in Table I.
TABLE I
EXTRACTED PARASITIC PARAMETERS OF VARIOUS BEND STRUCTURES AS DEPICTED IN Fig. 2.

<table>
<thead>
<tr>
<th>Bend Structure</th>
<th>( L_{13} ) (nH)</th>
<th>( L_{24} ) (nH)</th>
<th>( L_{36} ) (nH)</th>
<th>( C_{31} = C_{33} ) (pF)</th>
<th>( C_{22} = C_{44} ) (pF)</th>
<th>( C_{12} = C_{34} ) (pF)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Right-Angle Bent</td>
<td>0.058</td>
<td>1.889</td>
<td>0.053</td>
<td>0.052</td>
<td>0.271</td>
<td>0.028</td>
</tr>
<tr>
<td>Right-Angle with Mitre Bend</td>
<td>0.139</td>
<td>2.014</td>
<td>0.011</td>
<td>0.030</td>
<td>0.248</td>
<td>0.013</td>
</tr>
<tr>
<td>Round-Corner Bend</td>
<td>0.104</td>
<td>1.746</td>
<td>0.079</td>
<td>0.038</td>
<td>0.217</td>
<td>0.024</td>
</tr>
<tr>
<td>45-Degree-Angle Bend</td>
<td>0.181</td>
<td>0.961</td>
<td>0.092</td>
<td>0.024</td>
<td>0.118</td>
<td>0.017</td>
</tr>
</tbody>
</table>

Fig. 3. Simulation structure for the bend differential transmission lines.

The \( S \)-parameters obtained directly from the field solver and those deduced from the extracted lumped circuit model are in good agreement, although not shown here.

Given the equivalent circuit model, it is easy to simulate the signal integrity effects due to the bend discontinuities by a circuit simulator. A simulation structure for bent differential signal lines of length \( \ell = 25 \text{ mm} \) is depicted in Fig. 3, where the lumped equivalent circuit model is substituted for the bend discontinuities. Two drivers launch the differential ramped step signal with the magnitude of \( \pm 1 \text{ V} \) and rise time of 100 ps onto the coupled transmission lines. Furthermore, each voltage source is in series with an internal odd-mode resistance of 50 \( \Omega \), while the other end of the signal lines are connected with matched loads.

In this paper, the common-mode voltage \( V_c \), and differential-mode voltage \( V_d \), are respectively defined by

\[
\begin{bmatrix}
V_c \\
V_d
\end{bmatrix} = [T_{\text{im-od}}] \begin{bmatrix}
V_1 \\
V_2
\end{bmatrix} = \begin{bmatrix}
1/2 & 1/2 \\
1 & -1
\end{bmatrix} \begin{bmatrix}
V_1 \\
V_2
\end{bmatrix}
\]  

(5)

where \([T_{\text{im-od}}]\) is a \( 2 \times 2 \) matrix denoting the transformation from line voltages to modal voltages. For the bent differential transmission lines, transient analyses of the common-mode noise at the receiver and the reflected differential-mode noise at the sending end are shown in Fig. 4. Because of the length mismatch, asymmetrical interconnect, and differential bend, there is a difference of time delay between the two adjacent routing traces. The differential phase skew results in the conversion from differential to common mode and then the common-mode noise is induced [13].

In reference to Fig. 4, the 45-degree-angle bend structure, therefore, has the minimum common-mode noise and reflected differential-mode noise. In contrast to the voltage fluctuation, the other three bend structures have larger noise. The reduction amount of the common-mode noise induced by the 45-degree-angle bend is about 60% of that induced by the right-angle bend. As for the round-corner bend against the right-angle bend, its reflected differential-mode noise at the sending end can be somehow reduced, although the common-mode noise at the receiver is hardly alleviated. This is attributed to the large phase skew in comparison with that of the 45-degree-bend, though the discontinuity impedance of round-corner bend is improved.

For the signal integrity effects of various rise-time settings on the common-mode and reflected differential-mode noise, the transient analyses are shown in Fig. 5. It is found that the faster the rise-time setting of an input signal is fed, the higher both the common-mode noise and reflected differential-mode noise are observed. Empirically, while the rise time is lower than 100 ps with its corresponding frequency at 0.35/100 ps \( \approx 3.5 \text{ GHz} \) for the high-speed interconnect, the design encounters significant noise at the bend discontinuities.

IV. NOISE REDUCTION SCHEME ANALYSES

A. Dual Back-to-Back Coupled Bends

Due to the same velocity of even-mode and odd-mode signals, the stripline structure can cancel the common-mode noise but the microstrip structure cannot. A common practical routing scheme using dual back-to-back coupled bends with arbitrary angle \( \theta \) from 0° to 90° as depicted in Fig. 6 is employed to maintain the same trace length without the significant skew.

For the same parameters of cross section as in Fig. 2(e), the simulation is performed for the differential traces with the outside length \( \ell_1 \) and inner length \( \ell_2 \) of the dual bends. A differential ramped step signal with the magnitude of \( \pm 1 \text{ V} \) at the rise time of 1 ns is used for the excitation. Consider the dual
Fig. 4. Transient analyses for various bend structures at the rise time of 100 ps. (a) Common-mode noise at the receiver. (b) Reflected differential-mode noise at the sending end.

back-to-back coupled right-angle bends (when $\theta = 90^\circ$) with given outside length $\ell_1 = 26$ mm and various inner lengths $\ell = 50, 100, 150, 200, 250, \text{ and } 300$ mm as a parameter. It is found that the dual back-to-back coupled bends can help reduce the common-mode noise. The remnant common-mode noise at the probing point “g” for various length settings is shown in Fig. 7. However, the compensation becomes less effective if the length of coupled lines between the two coupled back-to-back bends increases.

While the routing length of the coupled line between dual back-to-back coupled bends increases, the velocity difference between the odd-mode and even-mode induced by the lower portion of differential bends and the remnant common-mode noise at the probing point “g” will increase. Nonetheless, the remnant common-mode noise will increase to become saturated until the difference in propagation time between the even-mode and odd-mode signals along the length $\ell$ is equal to twice the difference of delay time, $2\tau = 2 \times (T_{d2} - T_{d1})$, between the two traces of coupled bends. Note that $T_{d2}$ is the time delay observed on the outer corner and $T_{d1}$ for the time delay on the inner corner as depicted in Fig. 6. After the remnant common-mode noise has reached the saturation, a larger length $\ell$ will increase the transient width of noise.

Fig. 5. Transient analyses for the right-angle bend at various rise-time settings. (a) Common-mode noise at the receiver. (b) Reflected differential-mode noise at the sending end.

Fig. 6. Routing scheme and parameters of dual back-to-back coupled bends.
More quantitatively, let the excitation be a differential ramped step signal with magnitude $V_{in}$ and rise time $t_r$. The modal voltages at point “a” of Fig. 6 can be given by

$$
\begin{bmatrix}
V_c \\
V_d
\end{bmatrix}_a = \begin{bmatrix}
0 \\
-2V_0(t)
\end{bmatrix}
$$

$$
V_0(t) = \frac{V_{in}}{t_r}[t \cdot u(t) - (t - t_r) \cdot u(t - t_r)]
$$

(6)

where $u(t)$ is the unit step function. To facilitate the analysis, we neglect the effects of bend discontinuities and consider the delay effect. Cascading the effects of the bend, coupled lines, and bends following the signal trace, the modal voltage at the exit point “g” can be given by

$$
\begin{bmatrix}
V_c \\
V_d
\end{bmatrix}_g = [T_{m-\ell}]^{-1} \begin{bmatrix}
D(T_{d1}) & 0 & 0 & 0
\end{bmatrix} \begin{bmatrix}
D(T_{d2}) & 0 & 0 & 0
\end{bmatrix} [T_{m-\ell}]^{-1} \begin{bmatrix}
V_c \\
V_d
\end{bmatrix}_a
$$

(7)

where the matrix $[T_{m-\ell}]$ is defined in (5) and $D$ is a delay operator with the delay time defined in the parenthesis. The delay times $T_{d1}$ and $T_{d2}$ are given in Fig. 6, while $T_{even} = \ell/\nu_{even}$ and $T_{odd} = \ell/\nu_{odd}$ denote the propagation time of the common and differential modes along the inner length $\ell$, respectively.

The modal voltages at the exit point “g” can thus be related to those at the entrance point “a” by the explicit matrix multiplication in (7). By applying the incident signal in (6), the coupled common mode voltage at point “g” can be calculated as

$$
V_{cat \ point \ g} = \frac{1}{4} \left\{ D(T_{odd}) - D(T_{even}) \right\} \cdot \left\{ D(2T_{d1}) - D(2T_{d2}) \right\} \cdot V_0(t).
$$

(8)

In common cases that $T_{d1}, T_{d2} << t_r$, the maximum coupled common-mode noise as yielded by (8) can be given by

$$
V_{\text{common-mode \ MAX}} = \frac{V_{\text{in}}}{4t_r} \cdot \min\{2\tau, \Delta t\}
$$

(9)

where the time difference $\Delta t \equiv T_{even} - T_{odd}$ is related to the velocity difference percentage

$$
\Delta \nu(\%) = \frac{\nu_{odd} - \nu_{even}}{\nu_{average}} \times \frac{1}{\sqrt{1/C_{11}}(k_L - k_C)}
$$

(10)

by

$$
\Delta t = \frac{\ell}{\nu_{average}} \times \Delta \nu(\%)
$$

(11)

in which $\nu_{odd}$ and $\nu_{even}$ are the velocities of the common and differential modes, $\nu_{average}$ is their average, and $k_C$ and $k_L$ are the capacitive and inductive coupling coefficients, respectively.

It is clear from (9) that the maximum common-mode noise is proportional to the separation $\ell$ between the two bends, while it becomes saturated for larger $\ell$ such that $\Delta t > 2\tau$. In case of back-to-back coupled bends with arbitrary angle $\theta$ as depicted in Fig. 6, the difference of delay time $\tau$ in (9) is given by

$$
\tau = \frac{2\Delta \ell}{\nu_{average}} = \frac{2(w + s) \cdot \tan(\frac{\theta}{2})}{\nu_{average}}
$$

(12)

where $w$ and $s$ are the width and spacing of the coupled lines, respectively.

Another common practical routing scheme using a small detour for the inner trace of coupled bends, as depicted in Fig. 8, is also employed to maintain the same trace length. The analysis and results are similar to those for the dual back-to-back coupled bends. The remaining common-mode noise at point “g” will increase as the inner length $\ell$ becomes larger. However, it will become saturated when the difference in propagation time between the even-mode and odd-mode signals along the length

Fig. 7. Remnant common-mode noise of the dual back-to-back coupled bends observed at the probing point g for various length $\ell$ as depicted in Fig. 6.
The common-mode noise at the probing “g” can also be calculated by formulation, as in (9).

B. Compensation Capacitance for Noise Reduction

The common-mode conversion is primarily due to the skew between the two traces of coupled bends, and, therefore, how to let the two traces have the same skew is one way to reduce the effect of common-mode conversion. An efficient way is to shunt a compensation capacitance at the bends, as shown in the inset of Fig. 9, in order to add some extra time delay for the signal along the inner short path. For the same simulation structure as in Fig. 3 with the cross-sectional parameters of Fig. 2(e), the transient common-mode noise at the receiver for various values of compensation capacitance is presented in Fig. 9.

From the scattering parameters in (3), it is not difficult to obtain the mode conversion parameter [14]

$$S_{2d1} = \frac{1}{2} (S_{31} - S_{32} + S_{41} - S_{42})$$

(13)

If suitable compensation capacitances ($C_C' = 1/2C_C$) are shunted into the two ends of the inner bend, the mode conversion parameter $S_{2d1}$ can be minimized. After some algebraic simplification, the sufficient condition for the nearly zero $S_{2d1}$ is

$$\alpha_{13} - \alpha_{24} = 0.$$  

(14)

For a bend structure with compensation capacitances, formula (4) is still applicable, except that $C_{11,S}$ should be replaced by $C_{11,S}' = C_{11,S} + C_C'$. From (4a), (4b), (4d), and (4e), (14) can be expressed in terms of the equivalent circuit parameters and the compensation capacitance. By neglecting the small loss terms of $R_{ij}$ and $G_{ij}$, (14) can be satisfied if the compensation capacitance ($C_C$) is chosen as

$$C_C = 2 \times \left( \frac{L_{24} - L_{13}}{2Z_{0\text{ddl}}} - C_{11} \right)$$

(15)

where

$$Z_{0\text{ddl}} = \sqrt{\frac{L - L_{\text{in}}}{C + C_{\text{in}}}}$$

denotes the odd-mode impedance of differential lines.

For the aforementioned right-angle bend structure with $L_{13} = 0.058 \ \text{nH}$, $L_{24} = 1.889 \ \text{nH}$, $C_{11} = 0.052 \ \text{pF}$, $C_{22}' = 0.271 \ \text{pF}$, and $Z_{0\text{ddl}} = 50 \ \Omega$ as listed in Fig. 9, the optimal compensation capacitance can be calculated to be $1.17 \ \text{pF}$. The simulated results in Fig. 9 validate the correctness of (15). The maximum common-mode noise versus the compensation capacitance is listed in the lower-right corner of Fig. 9 to clarify the scheme of noise reduction. It is noted that the optimal compensation capacitance of $C_C = 1.17 \ \text{pF}$ can reduce the maximum common-mode noise by $(0.1092 \ V - 0.0508 \ V)/0.1092 \ V = 53.48\%$ against that without a compensation capacitance for right-angle bend discontinuities.

Since the practical layout routing size is very small, it is difficult to shunt a discrete capacitor to the inner corner of the coupled bends. A patch metal, such as a square patch for right-angle coupled bends, as shown in the inset of Fig. 10(a), and a fan-shaped patch for 45-degree coupled bends in Fig. 10(b), act as the parallel-plate capacitance to substitute for the discrete capacitor. For the aforementioned cross-section parameters and simulation structures of coupled bends as depicted in Fig. 2, the side length of a square patch is $x = 6.8 \ \text{mm}$ and the radius of a fan-shaped patch is $r = 4 \ \text{mm}$, respectively, using the parallel-plate capacitance formula

$$C_C = \varepsilon_0 \varepsilon_r \frac{A}{d}$$

(16)

for the optimal compensation capacitance of $C_C = 1.17 \ \text{pF}$. Here, $\varepsilon_0$ is the permittivity of free space, $\varepsilon_r$ is the substrate dielectric constant, $A$ and $d$ are the area and height of the parallel plate capacitance, respectively.

To validate the correctness of adding the parallel-plate patch and the capacitance formula (16), the equivalent circuit model parameters are extracted by employing the full-wave solver IE3D and applying the methods mentioned in Sections II and III. Then, the time- and frequency-domain simulations are performed by HSPICE and Microwave Office, respectively. The simulated waveforms for the coupled common-mode noise in Fig. 10 depict that the insertion of compensation capacitance is helpful for noise reduction. For the right-angle bends, a square
patch of the side lengths $x = 5.8\,\text{mm}$ can be the acceptable choice in reference to Fig. 10(a). For 45-degree bends with a fan-shaped patch, the radius corresponding to the maximum common-mode reduction is $r = 4\,\text{mm}$, as shown in Fig. 10(b).

This is in good agreement with the previous prediction (16). In comparison with the noise profiles of coupled bends without a compensation capacitance, the present simple design achieves reduction in common-mode noise by 58% and 46% for right- and 45-degree angled bends, respectively.

Fig. 11(a) shows the simulated differential-to-common mode conversion parameter, $S_{21}$, for 45-degree-angle bends with/without a fan-shaped patch. The reduction scheme by using a detour in Fig. 8 is also included for comparison, using parameters $L = 4\,\text{cm}$, $r_1 = 0.4\,\text{mm}$, and $r_2 = 2.15\,\text{mm}$. It can be found that both approaches, that with the detour and that with fan-shaped patch, can reduce the differential-to-common mode conversion. The reduction in mode conversion is about 10 dB for the detour approach. For the bend structure with a fan-shaped patch, the patch radius of $r = 4\,\text{mm}$ has the smallest mode conversion, while the noise reduction is frequency dependent.

On the other hand, neither the detour or patch compensation approach is successful in reducing the return loss for differential mode, as depicted by the simulation results in Fig. 11(b). The addition of a compensation patch may even result in a slight increase in the return loss over some frequency range. There is some tradeoff in the choice of the parallel-plate patch capacitance to reduce the differential-to-common mode conversion while not adversely affecting the differential mode return loss.

V. EXPERIMENTAL VALIDATION

A device under test (DUT) of coupled microstrip lines with right-angle bends as depicted in the inset of Fig. 12 was designed and fabricated to investigate the effect of differential discontinuity. The cross-sectional view and parameters of the structure...
are stated in Fig. 2(e). As in the aforementioned methods in Sections II and III, the equivalent circuit model parameters are extracted and the simulation results in frequency-domain are obtained by using Microwave Office.

The experimental verification for DUT is performed on the frequency-domain network analyzer Agilent/E5071B. Fig. 12 shows the simulation and experimental results of S-parameters, which are quite closely correlated to each other in the frequency range within 4 GHz. At higher frequencies beyond 6 GHz, the equivalent circuit model for this texture will be gradually insufficient for the analysis of strongly coupled bend discontinuities. From what has been discussed above, it is clear that the consistency between the simulation and measured results validates the equivalent circuit model and analysis approach.

Following the same experimental setup, the measured results of the mixed-mode S-parameters for 45-degree-angle coupled bends with a fan-shaped patch of \( r = 4 \) mm are shown in Fig. 13. They are compared favorably with the simulation results in the frequency range within about 4 GHz, while some deviations become prominent at high frequencies due to the deficiency of the equivalent circuit model. This experiment demonstrates that insertion of a fan-shaped patch of \( r = 4 \) mm can significantly suppress the differential-to-common mode conversion for the 45-degree-angle coupled bends, as evident from Fig. 13(a). The resultant increase in the differential return loss is small in most frequency ranges, as shown in Fig. 13(b).

VI. CONCLUSION

Based on the cascade property of the \( ABCD \) matrix, a systematic procedure has been established to extract the lumped circuit model of bend discontinuities from the calculated full-wave S-parameters. The circuit model can be applied to investigate the signal integrity issues of bend discontinuities in a high-speed interconnect design by the circuit simulator. The round-corner bends can reduce the reflection at the sending end but can hardly improve the common-mode noise at the receiver. The reduction amount of the common-mode noise induced by 45-degree-angle bends is about 60% of that induced by the right-angle bends. Note that the right-angle bends with and without miter are almost attributed to the maximum common-mode noise at the receiver. In addition, when the rise time of input signals is fed more quickly, the higher the common-mode noise is detected.

Based on the cascaded circuits, two traditional ways to reduce the common-mode noise in use of the dual back-to-back bends and bend with detour have been investigated. The compensation for noise reduction becomes less effective if the lengths of coupled lines between the dual back-to-back bends and between bend and detour increase. While the routing length increases, the remnant common-mode noise will increase and become saturated. To circumvent this routing length dependence and minimize the mode conversion parameter of bend discontinuities, a new compensation approach has been proposed and verified in this paper. The optimal value of a shunt compensation capacitance can be added so as to reduce the common-mode noise.
at the receiver. Because the routing size of a practical layout is very small, a parallel-plate patch metal is constructed to act as the compensation capacitance. Feasible schemes using a square very small, a parallel-plate patch metal is constructed to act as the compensation capacitance. Favorable comparisons between the simulation and measurement in frequency domain validate the equivalent circuit model, the analysis method, and the effectiveness of the compensation path approach. The design methodology described in this paper is therefore useful for noise reduction concerned in the high-speed multigigabit per second digital circuitry.

REFERENCES


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